

JUN 30 2006

S/N 09/897,574

PATENTIN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant:	Kawaguchi	Examiner:	K. Huynh
Serial No.:	09/897,574	Group Art Unit:	2112
Filed:	July 2, 2001	Docket No.:	10873.0744US01
Title:	DATA TRANSFER APPARATUS AND DATA TRANSFER METHOD		

CERTIFICATE UNDER 37 CFR 1.6(d):

I hereby certify that this paper is being transmitted by facsimile to the U.S. Patent and Trademark Office on June 30, 2006.

By: 

Name: Chieko Rogers

Mail Stop AF
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

REPLACEMENT RESPONSE

Dear Sir:

Applicant requests that the Response filed on June 21, 2006 be withdrawn as having been filed in error and consider this Response in its place. Applicant appreciates the courtesy of the Examiner in discussing this matter with the undersigned on June 27, 2006. At that time the Examiner confirmed that the June 21, 2006 paper had not been considered and would not be considered.

In response to the Office Action mailed March 21, 2006, favorable reconsideration of this application is requested in view of the following remarks.

Applicants appreciate the courtesy extended by the Examiner to Applicants' representatives, Douglas P. Mueller and Margaret F. Emerson, during the telephonic interview on June 8, 2006. During the interview, claims 1-14 and West (US 6,195,730) were discussed. Agreement regarding the allowability of the claims was not reached, although agreement to further consider the claims in light of the discussed arguments was reached. A summary of the discussed arguments follows. Applicant notes that other features of the claimed invention were discussed during the interview. Since they are not related to the rejection as stated in the Office

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Action and are not being relied upon for the purposes of this Response, the details of those discussions are not included herewith.

Claims 1-14 were rejected as being anticipated by West (US 6,195,730). Applicant traverses this rejection. West does not disclose or suggest a data transfer apparatus being a first device including a controller for controlling data input/output of an associative memory, wherein the controller fetches an address and data that are transferred between a **second** device and a **third** device that are connected only on the system bus so as to duplicate and store them in the associative memory, as required by claims 1, 6, and 11-14. West teaches an IOP (60) that includes an IOP microprocessor (44, rejection equates to the claimed controller), an IOP local memory (46, rejection equates to the claimed associative memory), and a cache memory (62, rejection seems to also equate to the claimed associative memory). However, West teaches that the IOP local memory (46) only fetches address and data when transferring between its own IOP (60, rejection equates to the claimed first device) and another IOP (60, rejection equates to the claimed second device). West does not teach that either the IOP local memory (46) or the cache (memory) stores or buffers address and data transferred between two other IOPs provided off of the system bus (28), as is required by claims 1, 6, and 11-14. In fact, the purpose of the IOP taught by West is to mirror what is contained in the attached storage device (22, rejection equates to the claimed fourth device). See Figure 2.

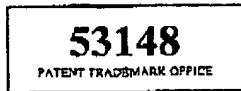
Favorable reconsideration of claims 1-14 is requested.

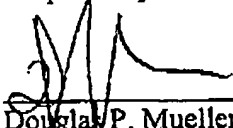
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In view of the above, favorable reconsideration in the form of a notice of allowance is requested. Any questions regarding this communication can be directed to the undersigned attorney, Douglas P. Mueller, Reg. No. 30,300, at (612)455-3804.

Respectfully Submitted,

Dated: June 30, 2006




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FROM: Douglas P. Mueller

OUR REF: 10873.0744US01

TELEPHONE: (612) 455.3800

Total pages, including cover letter: 5

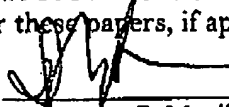
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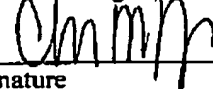
Applicant: KAWAGUCHI
Serial No.: 09/897574
App. Filed: July 2, 2001
Group Art No.: 2112

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By: 
Name: Douglas P. Mueller
Reg. No.: 30,300

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